

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electronic endoscope selector comprising:

a video signal switching processor that switches video signals, which are output to at least one peripheral device, from first video signals fed from a first electronic endoscope to second video signals fed from a second electronic endoscope;

a synchronizing signal switching processor that switches synchronizing signals, which are output to said peripheral device, from synchronizing signals fed from said first electronic endoscope to synchronizing signals fed from said second electronic endoscope, wherein said synchronizing signal switching processor completes synchronization of the synchronizing signals before said video signal switching processor outputs the video signals to said peripheral device; and

a switching control processor that drives said video signal switching processor and said synchronizing signal switching processor, and suspends output of said video signals for a predetermined period while the switched synchronizing signals are output, the predetermined period being longer than the time required for the synchronizing signal to synchronize with said peripheral device.

2. (Original) A selector according to claim 1, comprising an operating processor that is used for operating the driving of said switching control processor.

3. (Original) A selector according to claim 1, wherein said switching control processor drives said video signal switching processor and said synchronizing signal

switching processor simultaneously.

4. (Original) A selector according to claim 3, wherein said switching control processor comprises:

an output switching processor that switches output of said video signals between ON and OFF states, wherein said ON state permits output of said video signals and said OFF state forbids output of said video signals;

a timer for timing said predetermined period; and

wherein said output switching processor is set to said OFF state and said timer and said synchronizing signal switching processor are started simultaneously, and after said predetermined period, said output switching processor is switched to said ON state.

5. (Original) A selector according to claim 4, wherein said output switching processor switches said ON and OFF states for outputting video signals from said video signal switching processor.

6. (Original) A selector according to claim 1, wherein said switching control processor drives said video signal switching processor for a predetermined period after driving said synchronizing signal switching processor, and suspends output of said video signals during said predetermined period.

7-8. (Canceled)